In the margin (handwritten, vertical): DO NOT ENTER TM 10/14/05

In the claims:

For the Examiner's convenience, all pending claims are presented below with changes shown in accordance with the mandatory amendment format.

1.   (Currently Amended) A method of translating instructions, said method comprising:

translating a first block of instructions executable in a first processor architecture, into a translated first block of instructions executable in a second processor architecture, said translated first block of instructions operating with a stack of data entry positions; and

during the translating, generating an expected Top of Stack (TOS) position in said stack for said first block of instructions ~~code~~; and

during the translating, adding at least one instruction to said translated first block of instructions to determine if said first expected TOS is equal to an actual TOS <u>position in said stack</u> at a time of executing said translated first block of instructions<u>;</u>

<u>wherein said at least one instruction branches to correction code if said expected TOS is not equal to said actual TOS, and</u>

<u>wherein said correction code to generate a delta of said expected TOS and said actual TOS and to adjust said stack for said first block of instructions by the delta at the time of executing said translated first block of instructions.</u>

2.   (Canceled)

3.   (Canceled).